

Dual Digital Transistor (NPN+NPN)

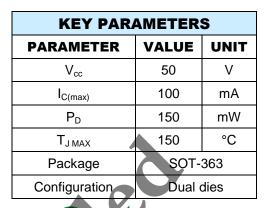
FEATURES

- Epitaxial planar die construction
- Surface device type mounting
- Two DTC114E chip in a package
- Transistor elements are independent, eliminating interference
- Mounting cost and area be cut in half
- Moisture sensitivity level: level 1, per J-STD-020
- **RoHS Compliant**
- Halogen-free according to IEC 61249-2-21

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- Inverter circuit
- Interface circuit
- Driver circuit

MECHANICAL DATA

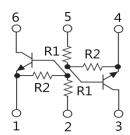




HALOGEN FREE



SOT-363



Interface circuitDriver circuit		Pb-Free COMPLIANT	77.22
 MECHANICAL DATA Case: SOT-363 Terminal: Matte tin plated leads, see Polarity: As marked Weight: 7.10mg (approximately) 	olderable per J-STD-002	SOT-363	40
ABSOLUTE MAXIMUM RAT	TINGS (T _A = 25°C unle	R2 R1 1 2 ss otherwise noted) UMH11N	UNIT
	STWBUL		UNII
Marking code on the device		H11	
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10 - 40	V
Output current	Io	50	mA
Output current	I _{C(max)}		
	O(max)	100	mA
Power dissipation	P _D	100 150	
Power dissipation Junction temperature range			mA



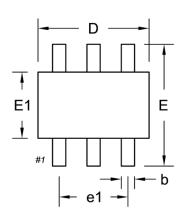
ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Input voltage	$V_{CC} = 5V, I_{O} = 100uA$	V _{I(off)}	0.5	-	-	V
Input voltage	$V_O = 0.3V$, $I_O = 10mA$	V _{I(on)}	-	5 3 - 3 0.1 0.3 - 0.88 - 0.5	3	V
Output voltage	I _O / I _I = 10mA / 0.5mA	V _{O(on)}	-	0.1	0.3	V
Input current	V _I = 5V	I _I	-	-	0.88	mA
Onput current	$V_{CC} = 50V, V_{I} = 0V$	I _{O(off)}	-	-	0.5	uA
DC current gain	$V_0 = 5V, I_0 = 5mA$	Gı	30	- (\ -	-
Input resistance		R ₁	7	10	13	kΩ
Resistance ratio		R ₂ /R ₁	0.8	10	1.2	-
Transition frequency	$V_{CE} = 10V,$ $I_{E} = 5mA, f = 100MHz$	f _⊤	-	250	_	MHz

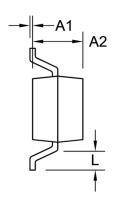
RING INFORMATION PROBLEM PACKAGE UMH11N REG SOT-363
UMH11N REG SOT-363



PACKAGE OUTLINE DIMENSIONS

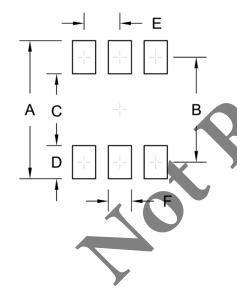
SOT-363





DIM.	Unit	(mm)	Unit (inch)		
Dilvi.	Min.	Max.	Min.	Max.	
A1	0.00	0.10	0.000	0.004	
A2	0.85	1.05	0.033	0.041	
b	0.15	0.35	0.006	0.014	
D	2.00	2.20	0.079	0.087	
E	2.15	2.45	0.085	0.096	
E1	1.15	1.35	0.045	0.053	
e1	1.20	1.40	0.047	0.055	
L	0.25	0.46	0.010	0.018	

SUGGESTED PAD LAYOUT



/) <u> </u>		2.10	2.10	0.000	0.000
Ĺ L	E1	1.15	1.35	0.045	0.053
<u>†</u>	e1	1.20	1.40	0.047	0.055
	L	0.25	0.46	0.010	0.018
ΙΤ					
O y	Symbo	I U	nit (mm)	Uni	t (inch)
	Α		2.50	0.	.098
	В		1.90	0.	.075
	С		1.30	0.	.051
			0.60		
	D		0.00	0	.024
	E		0.65		.024



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